

Difference Predicted Trace Compression on FPGAs

Wednesday, 18 October 2017 14:05 (20 minutes)

A compression scheme for trace data, the output of sampling ADCs, which is suitable for implementation on front-end FPGAs will be presented.

Primary author: BRUNI, Giovanni (Chalmers University of Technology)

Co-author: Dr JOHANSSON, Håkan (Chalmers)

Presenter: BRUNI, Giovanni (Chalmers University of Technology)

Track Classification: SFAIR